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S.I.  
10-15-03  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Tsuyoshi YONEYAMA

Application No.: 10/062,422

Filed: February 5, 2002

Docket No.: 111907

For: DISPLAY DRIVE CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT, DISPLAY PANEL, AND DISPLAY DRIVE METHOD

INFORMATION DISCLOSURE STATEMENT

RECEIVED

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

OCT 07 2003

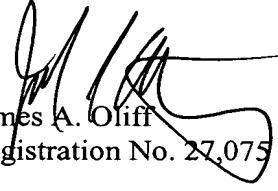
Technology Center 2600

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. English-language Abstracts of the non-English language references 1-8 are attached hereto.
- ☒ 3. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See References 1-2, and 6-7.

Respectfully submitted,

  
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JAO:JSA/emt  
Date: October 2, 2003

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